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L2	105	((allocat\$3) with (process\$3 application task\$3 job) with ((multiprocessor cpu processor (process\$3 adj unit)) with (time cycle consum\$5 usage)) with (percent\$3 ratio shar\$3 portion number) with ((processor multiprocessor cpu nod\$2 server (processing adj unit)) with (group\$3 set)))	US-PGPUB; USPAT; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:54
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L4	4	11 and 718/102.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/10/28 15:55
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Last Name = DOROFEEV

First Name = ANDREI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10661413	Not Issued	20		Processor specific dispatching in a heterogeneous configuration	DOROFEEV, ANDREI
09843426	Not Issued	71		Apparatus and method for scheduling processes on a fair share basis	DOROFEEV, ANDREI V.
10768303	Not Issued	30		Mechanism for associating resource pools with operating system partitions	DOROFEEV, ANDREI V.
10771827	Not Issued	30		Multi-level computing resource scheduling control for operating system partitions	DOROFEEV, ANDREI V.

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Last Name = TUCKER First Name = ANDREW

			·····		
Application#	Patent#	Status	Date Filed	Title	Inventor Name
08675236	5937187	150	07/01/1996	METHOD AND APPARATUS FOR EXECUTION AND PREEMPTION CONTROL OF COMPUTER PROCESS ENTITIES	TUCKER, ANDREW
60290057	Not Issued	159	05/10/2001	Fruit/vegetable paring apparatus	TUCKER, ANDREW A.
60590336	Not Issued	159	07/23/2004	Filter	TUCKER, ANDREW C.
09843426	Not Issued	71		Apparatus and method for scheduling processes on a fair share basis	TUCKER, ANDREW G.
10661413	Not Issued	20		Processor specific dispatching in a heterogeneous configuration	TUCKER, ANDREW G.
10744360	Not Issued	30		System logging within operating system partitions	TUCKER, ANDREW G.
10761622	Not Issued	30	01/20/2004	Two-level service model in operating system partitions	TUCKER, ANDREW G.
10762066	Not Issued	30		System statistics virtualization for operating system partitions	TUCKER, ANDREW G.
10762067	Not Issued	20		System accounting for operating system partitions	TUCKER, ANDREW G.
10763147	Not Issued	20		Global visibility controls for operating system partitions	TUCKER, ANDREW G.
10766094	Not Issued	20		Interprocess communication within operating system partitions	TUCKER, ANDREW G.
10767003	Not Issued	30	01/28/2004	Virtual system console for virtual application environment	TUCKER, ANDREW G.
10767118	Not Issued	20		Mechanism for implementing a sparse file system for an operating system partition	TUCKER, ANDREW G.
10767235	Not Issued	20	01/28/2004	Mechanism for selectively providing mount information to processes running within operating system partitions	TUCKER, ANDREW G.
10768303	Not Issued	30	01/29/2004	Mechanism for associating resource pools with operating system partitions	TUCKER, ANDREW G.

					18
10769415	Not Issued	30	11 1	Fine-grained privileges in operating system partitions	TUCKER, ANDREW G.
10771698	Not Issued	30	11	Multi-level resource limits for operating system partitions	TUCKER, ANDREW G.
10771827	Not Issued	30	02/03/2004	Multi-level computing resource scheduling control for operating system partitions	TUCKER, ANDREW G.
<u>10771870</u>	Not Issued	20	02/04/2004	Login methods for virtual application environments	TUCKER, ANDREW G.
10790988	Not Issued	30	03/02/2004	Virtual processor sets	TUCKER, ANDREW G.
10791023	Not Issued	20	III I	Interposing library for page size dependency checking	TUCKER, ANDREW G.
60469558	Not Issued	159	05/09/2003	Operating system virtualization	TUCKER, ANDREW G.
08767353	6223204	150	12/18/1996	USER LEVEL ADAPTIVE THREAD BLOCKING	TUCKER, ANDREW G.
08879150	6151639	150	06/19/1997	SYSTEM AND METHOD FOR REMOTE OBJECT INVOCATION	TUCKER, ANDREW G.
08879151	5808911	150	06/19/1997	SYSTEM AND METHOD FOR REMOTE OBJECT RESOURCE MANAGEMENT	TUCKER, ANDREW G.
08884978	6138251	150	06/30/1997	METHOD AND SYSTEM FOR RELIABLE REMOTE OBJECT REFERENCE MANAGEMENT	TUCKER, ANDREW G.
09058406	6185695	150		METHOD AND APPARATUS FOR TRANSPARENT SERVER FAILOVER FOR HIGHLY AVAILABLE OBJECTS	TUCKER, ANDREW G.
09059406	Not Issued	161	04/14/1998	WASHING MACHINE	TUCKER, ANDREW G.
06703797	4583590	150	02/21/1985	SLIP RELEASING APPARATUS	TUCKER, ANDREW J.
07100303	4813486	150	09/23/1987	RETRACTABLE SLIP ASSEMBLY	TUCKER, ANDREW J.
07883632	5314014	150	05/04/1992	PACKER AND VALVE ASSEMBLY FOR TEMPORARY ABANDOMENT OF WELLS	TUCKER, ANDREW J.
08396410	5685574	150	02/28/1995	SNAP LATCH SEAL LOCATOR FOR SEALINGLY LATCHING TUBING TO A PACKER IN A WELLBORE	TUCKER, ANDREW J.
08918213	5988285	150	08/25/1997	ZONE ISOLATION SYSTEM	TUCKER, ANDREW J.
10455204	Not	93	06/05/2003	COOLED INSULATION SURFACE	TUCKER, ANDREW R.

	Issued		TEMPERATURE CONTROL SYSTEM	
10457594	Not Issued	41	 Actively cooled ceramic thermal protection system	TUCKER, ANDREW R.

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41 A Survey of Energy Efficient Network Protocols for Wireless Networks

Christine E. Jones, Krishna M. Sivalingam, Prathima Agrawal, Jyh Cheng Chen

September 2001

Wireless Networks, Volume 7 Issue 4

Publisher: Kluwer Academic Publishers

Full text available: pdf(271.55 KB)

Additional Information: full c

Wireless networking has witnessed an explosion of interest from consumers in recent years for its appl communication infrastructure, energy efficiency will be an important design consideration due to the lir Since the network interface is a significant consumer o ...

Keywords: energy efficient design, low-power design, mobile computing, network protocols,, power a

42 Building a distributed full-text index for the web

July 2001

ACM Transactions on Information Systems (TOIS), Volume 19 Issue 3

Publisher: ACM Press

Full text available: pdf(651.72 KB)

Additional Information: full c

We identify crucial design issues in building a distributed inverted index for a large collection of Web parametric time. We also propose a storage scheme for creating and managing inverted files using ar inverted indexes. Finally, we present pe ...

Keywords: Distributed indexing, Embedded databases, Inverted files, Pipelining, Text retrieval

43 Compiler-based I/O prefetching for out-of-core applications

Angela Demke Brown, Todd C. Mowry, Orran Krieger
May 2001

ACM Transactions on Cor

ACM Transactions on Computer Systems (TOCS), Volume 19 Issue 2

Publisher: ACM Press

Full text available: pdf(499.03 KB)

Additional Information: full c

Current operating systems offer poor performance when a numeric application's working set does not f onerous task of rewriting an application to use explicit I/O operations (e.g., read/write). In this paper, and requires only minima ...

Keywords: compiler optimization, prefetching, virtual memory

Data and memory optimization techniques for embedded systems



P. R. Panda, F. Catthoor, N. D. Dutt, K. Danckaert, E. Brockmeyer, C. Kulkarni, A. Vandercappelle, P. G. F. April 2001

ACM Transactions on Design Automation of Electronic Systems (TODAE!

Publisher: ACM Press

Full text available: pdf(339.91 KB)

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We present a survey of the state-of-the-art techniques used in performing data and memory-related of one or more out of three important cost metrics: area, performance, and power dissipation of the result cover a broad spectrum of optimizati ...

Keywords: DRAM, SRAM, address generation, allocation, architecture exploration, code transformation file, size estimation, survey

45 Symbiotic jobscheduling for a simultaneous multithreaded processor

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Allan Snavely, Dean M. Tullsen

November 2000 ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Arch languages and operating systems ASPLOS-IX, Volume 34, 28 Issue 5, 5

Publisher: ACM Press

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Simultaneous Multithreading machines fetch and execute instructions from multiple instruction streams hardware to support simultaneous execution, the operating system scheduler must choose the set of jointh are coscheduled by the operating system jobsche ...

46 An analysis of operating system behavior on a simultaneous multithreaded architecture

Joshua A. Redstone, Susan J. Eggers, Henry M. Levy

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ACM SIGARCH Computer Architecture News, ACM SIGOPS Operating Sylanguages and operating systems ASPLOS-IX, Volume 28, 34 Issue 5, 5

Publisher: ACM Press

Full text available: pdf(227.80 KB)

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This paper presents the first analysis of operating system execution on a simultaneous multithreaded (mode execution. However, many of the applications most amenable to multithreading technologies spe execution and measurement of the operating sy ...

47 System architecture directions for networked sensors



Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister

November 2000

ACM SIGOPS Operating Systems Review , ACM SIGARCH Computer Arch languages and operating systems ASPLOS-IX, Volume 34 , 28 Issue 5 , 5

Publisher: ACM Press

Full text available: pdf(299.01 KB)

Additional Information: full c

Technological progress in integrated, low-power, CMOS communication devices and sensors makes a ri environment like smart dust. The missing elements are an overall system architecture and a methodok design a tiny event-driven operating system, and sh ...

48 Symbiotic jobscheduling for a simultaneous mutlithreading processor



Allan Snavely, Dean M. Tullsen

ACM SIGPLAN Notices, Volume 35 Issue 11

Publisher: ACM Press

November 2000

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Simultaneous Multithreading machines fetch and execute instructions from multiple instruction streams hardware to support simultaneous execution, the operating system scheduler must choose the set of journal that are coscheduled by the operating system jobsche ...

49 An analysis of operating system behavior on a simultaneous multithreaded architecture

Joshua A. Redstone, Susan J. Eggers, Henry M. Levy

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System architecture directions for networked sensors

Jason Hill, Robert Szewczyk, Alec Woo, Seth Hollar, David Culler, Kristofer Pister

November 2000 ACM SIGPLAN Notices, Volume 35 Issue 11

Publisher: ACM Press

Full text available: pdf(1.32 MB)

Additional Information: full c

Technological progress in integrated, low-power, CMOS communication devices and sensors makes a ri environment like smart dust. The missing elements are an overall system architecture and a methodok design a tiny event-driven operating system, and sh ...

Scalable molecular dynamics for large biomolecular systems

Robert K. Brunner, James C. Phillips, Laxmikant V. Kale

November 2000 Proceedings of the 2000 ACM/IEEE conference on Supercomputing (CD

Publisher: IEEE Computer Society

Full text available: pdf(211.89 KB) Publisher Site

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We present an optimized parallelization scheme for molecular dynamics simulations of large biomolecu spatial decomposition scheme, and an aggressive measurement-based predictive load-balancing frame summarizes the broad methodology we are pu ...

Process migration

September 2000

ACM Computing Surveys (CSUR), Volume 32 Issue 3

Publisher: ACM Press

Full text available: pdf(1.24 MB)

Additional Information: full c

Process migration is the act of transferring a process between two machines. It enables dynamic load $\mathfrak c$ efforts, migration has not achieved widespread use. With the increasing deployment of distributed syst research and product development. As hi ...

Keywords: distributed operating systems, distributed systems, load distribution, process migration

Techniques for obtaining high performance in Java programs Iffat H. Kazi, Howard H. Chen, Berdenia Stanley, David J. Lilja

September 2000

ACM Computing Surveys (CSUR), Volume 32 Issue 3

Publisher: ACM Press

Full text available: pdf(816.13 KB)

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This survey describes research directions in techniques to improve the performance of programs writte portability of programs. A Java interpreter dynamically executes Java bytecodes, which comprise the ir compilation, possibly at the expense of portabili ...

Keywords: Java, Java virtual machine, bytecode-to-source translators, direct compilers, dynamic com

Improving interactive performance using TIPME

Yasuhiro Endo, Margo Seltzer June 2000

ACM SIGMETRICS Performance Evaluation Review, Proceedings of the

'00, Volume 28 Issue 1

Publisher: ACM Press

Full text available: pdf(1.05 MB)

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On the vast majority of today's computers, the dominant form of computation is GUI-based user intera user's perception of performance is affected by unexpectedly long delays. However, most performance of the system, they do littl ...

Keywords: interactive performance, monitoring

55 Recency-based TLB preloading

Ashley Saulsbury, Fredrik Dahlgren, Per Stenström

May 2000

May 2000 ACM SIGARCH Computer Architecture News , Proceedings of the 27th a

Publisher: ACM Press

Full text available: pdf(651.05 KB)

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Caching and other latency tolerating techniques have been quite successful in maintaining high memor are growing beyond the capacity of TLBs. This work presents one of the first attempts to hide TLB miss cut so ...

56 System-level power optimization: techniques and tools

Luca Benini, Giovanni de Micheli

April 2000 ACM Transactions on Design Automation of Electronic Systems (TODAE!

Publisher: ACM Press

Full text available: pdf(385.22 KB)

Additional Information: full c

This tutorial surveys design methods for energy-efficient system-level design. We consider electronic strength, namely computation, communication, and storage units, and we review methods of reducing the software design and compilation. This survery ...

57 Session summaries from the 17th symposium on operating systems principle (SOSP'99)

Jay Lepreau, Eric Eide

April 2000 ACM SIGOPS Operating Systems Review, Volume 34 Issue 2

Publisher: ACM Press

Full text available: 📆 pdf(3.15 MB) Additional Information: full citation, index terms

58 HPFBench: a high performance Fortran benchmark suite

Y. Charlie Hu, Guohua Jin, S. Lennart Johnsson, Dimitris Kehagias, Nadia Shalaby

March 2000 ACM Transactions on Mathematical Software (TOMS), Volume 26 Issue 1

Publisher: ACM Press

Full text available: pdf(274.52 KB)

Additional Information: full c

The high performance Fortran (HPF) benchmark suite HPFBench is designed for evaluating the HPF lang application kernels that reflect the computational structure and communication patterns in fluid dynam of FLOP count, memory usage, communi ...

Keywords: benchmarks, compilers, high performance Fortran

59 Borrowed-virtual-time (BVT) scheduling: supporting latency-sensitive threads in a general-purpos

Kenneth J. Duda, David R. Cheriton

December 1999 ACM SIGOPS Operating Systems Review , Proceedings of the seventeen

Publisher: ACM Press

Full text available: pdf(1.81 MB)

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Systems need to run a larger and more diverse set of applications, from real-time to interactive to bate specialized to complex real-time paradigms, limiting their applicability to general-purpose systems. In tapplications yet weighted sharin ...

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The interactive performance of SLIM: a stateless, thin-client architecture

Brian K. Schmidt, Monica S. Lam, J. Duane Northcutt

December 1999 ACM SIGOPS Operating Systems Review , Proceedings of the seventeen

Publisher: ACM Press

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